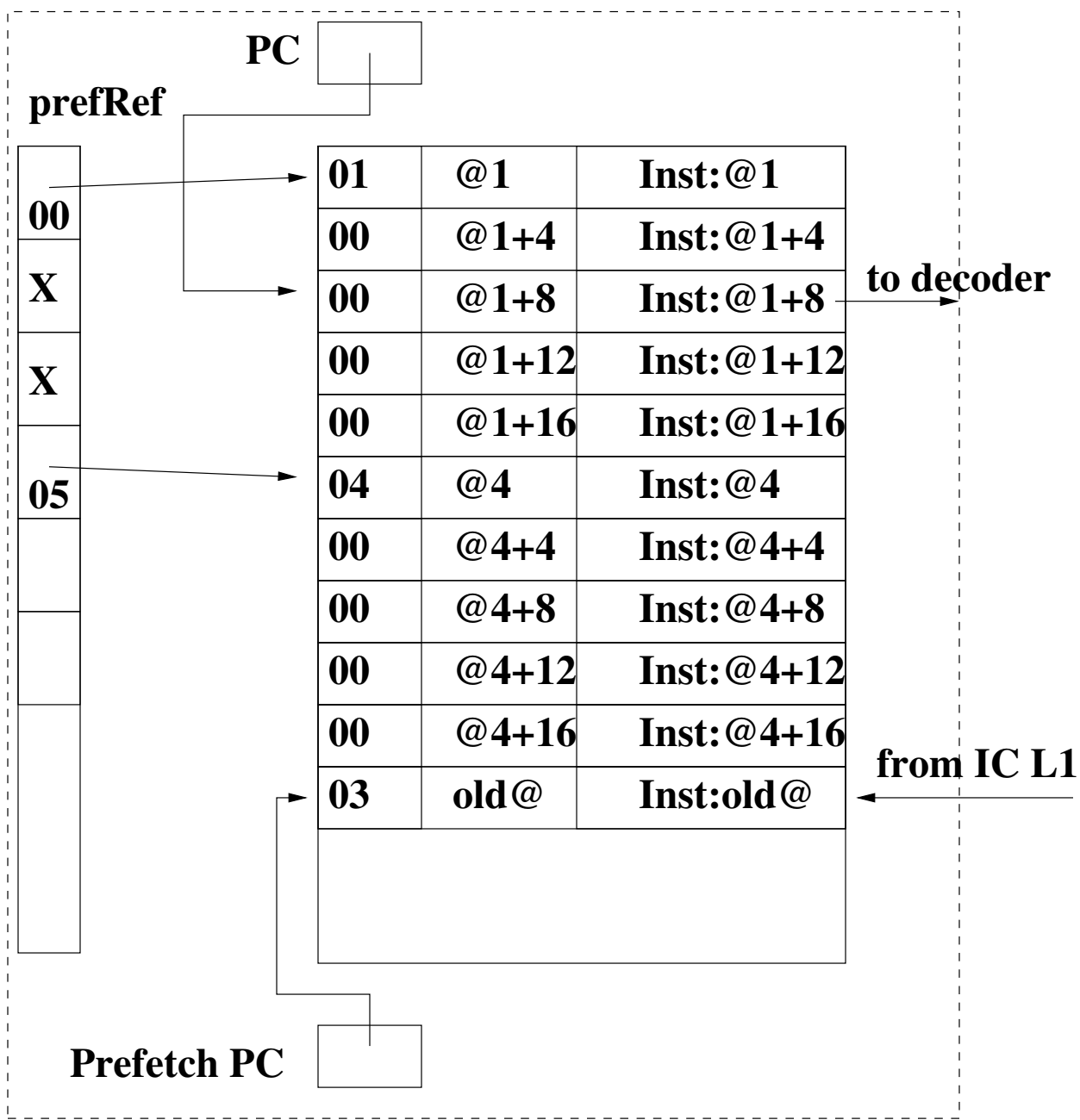


F-CPU Prefetcher (IC L0)

RegSet

1	@1
2	D2
3	D3
4	@4



Prefetcher algorithm:

if(miss)

fetch PC

if(loadaddri Ri,..)

prefetch @i

else

prefetch (Prefetch PC++

if(RihasChanged)

prefRef(i)=X