



P0[7]	1	J1	1
P0[5]	2	J1	2
P0[3]	3	J1	3
P0[1]	4	J1	4
P2[7]	5	J1	5
P2[5]	6	J1	6
P2[3]	7	J1	7
P2[1]	8	J1	8
SMP	9	J1	9
P1[7]	10	J1	10
P1[5]	11	J1	11
P1[3]	12	J1	12
IC_SCLK	13	J1	13
GND	14	J1	14

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VX	1	J2	1
P0[6]	2	J2	2
P0[4]	3	J2	3
P0[2]	4	J2	4
P0[0]	5	J2	5
P2[6]	6	J2	6
P2[4]	7	J2	7
P2[2]	8	J2	8
P2[0]	9	J2	9
XRES	10	J2	10
P1[6]	11	J2	11
P1[4]	12	J2	12
P1[2]	13	J2	13
IC_SDATA	14	J2	14

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